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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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EXAMINER	
FOTAKIS, ARISTOCRATIS	

ART UNIT	PAPER NUMBER
2611	

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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)	
	10/799,543	HAN ET AL.	
	Examiner	Art Unit	
	Aristocratis Fotakis	2611	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *11 March 2004*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1 - 26* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1 - 26* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on *11 March 2004* is/are: a)☒ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☒ Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date *03/11/2007*.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .
5) ☐ Notice of Informal Patent Application
6) ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

> The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 21 – 26 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Claims 21 and 24, recite the limitation "without also" in line 7. This is indefinite.

Claim Rejections - 35 USC § 101

35 U.S.C. 101 reads as follows:

> Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the conditions and requirements of this title.

Claim 7 is rejected under 35 U.S.C. 101 because the claimed invention is directed to non-statutory subject matter. Claim 7 recites "a machine-readable medium" which is directed to non-statutory subject matter.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

The factual inquiries set forth in *Graham* **v.** *John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.
3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Claims 1 – 13, 15 – 21, 23 – 24 and 26 are rejected under 35 U.S.C. 103(a) as being unpatentable over McEwen et al (US 6,732,328) in view of Akiyama et al (US 7,080,313).

Re claims 1, 7 and 17, McEwen teaches of a method and a machine-readable medium (Col 1, Lines 20 – 34, Fig.10) embodying information indicative of instructions for causing one or more machines to perform operations comprising: obtaining an output signal sequence (Y_K, Col 10, Lines 35 - 45) from a partial response channel (#17, Fig.2); determining an input sequence (Col 10, Line 55 - 57) of the partial response channel (#17, Fig.2) by maximizing a correlation metric (path metric corresponding to the maximum likelihood, V_{DATA}) from the obtained output sequence (Y_K, output from the partial channel response), and providing an output corresponding to the determined input sequence (Col 10, Lines 58 – 67, Fig.2). However, McEwen does not specifically show the correlation performed in the branch metric unit.

Akiyama teaches of a signal evaluation apparatus and signal evaluation method for evaluating a reproduction signal of a recording medium (Col 1, Lines 6 – 8). Akiyama teaches of a branch metric $(Z_K - Y_K)^2$ of each branch metrics (correlation) where Z_K is the obtained output sequences and Y_K is the estimated output sequences (Col 2, Lines 8 – 20) for a partial response channel (PRML, Col 1, Line 26 and Col 7, Lines 10 – 21); the estimated output sequence being estimated based on the partial response channel (Col 8, Lines 63 – 67);

It would have been obvious to one having ordinary skill in the art at the time the invention was made to have known that a branch metric unit would correlate the obtained output sequence with the estimated output sequence so that the survivor path memory would output the maximum likelihood sequence estimate V_{DATA}.

Re claim 13, McEwen teaches of an apparatus comprising: a branch metric generator (#53, BMU, Fig.4) that generates branch metrics comprising a correlation for a partial response channel (#17, Fig.2); an add-compare-select component (#55, ACS, Fig.4) that compares paths (#56, Fig.4) and determines survivor paths (#57, Fig.4) using generated branch metrics; a memory that retains metrics information (path memory, Col 2, Lines 3 - 4); and a trace-back component that determines a best path of the survivor paths and outputs sequence information based on the determined best path (Col 2, Lines 4 – 10). However, McEwen does not specifically show the correlation performed in the branch metric unit.

Akiyama teaches of a signal evaluation apparatus and signal evaluation method for evaluating a reproduction signal of a recording medium (Col 1, Lines 6 – 8). Akiyama teaches of a branch metric $(Z_K - Y_K)^2$ of each branch metrics (correlation) where Z_K is the obtained output sequences and Y_K is the estimated output sequences (Col 2, Lines 8 – 20) for a partial response channel (PRML, Col 1, Line 26 and Col 7, Lines 10 – 21); the estimated output sequence being estimated based on the partial response channel (Col 8, Lines 63 – 67);

It would have been obvious to one having ordinary skill in the art at the time the invention was made to have known that a branch metric unit would correlate the obtained output sequence with the estimated output sequence so that the survivor path memory would output the maximum likelihood sequence estimate V_{DATA}.

Re claims 2, 8 and 18, McEwen teaches of employing Viterbi detection (#50, Fig.2) using a cross-correlation branch metric (#53, Fig.4) (Col 10, Lines 62 – 65).

Re claims 3, 9, 15 and 19, McEwen teaches of the partial response channel (#17, Fig.2) having a transfer function (*H(D)*, Col 7, Lines 8 - 12) defined according to a target polynomial, $T(D)=p_0 +p_1D+...+P_M D^M$ (Col 7, Lines 8 - 12) the Viterbi detection operates according to a trellis having 2^M states ($2^L - 1$ states, Col 7, lines 47 – 60), and all survivor paths (associated with all the 2^M states in the trellis merge in *M* steps (Col 2, lines 3 – 4).

Re claims 4, 10 and 20, McEwen teaches of providing the output corresponding to the determined input sequence (from #50, Fig.2) comprises providing the determined input sequence to an additional sequence-processing component (#60, Fig.2) (Col 7, Lines 61 – 67 to Col 8, Lines 1 – 10).

Re claims 5 and 11, McEwen teaches of the output signal sequence (Y_K) comprising a convolution of the input sequence (Col 10, Line 55 - 57) and a target

polynomial (*IDEAL*$_K$, Col 10, Line 47) of the partial response channel (Col 10, Lines 46 – 57).

Re claims 6 and 12, McEwen teaches of the partial response channel comprising a data storage medium (Col 1, Lines 20 – 34, Fig.10), and said obtaining the output signal sequence comprises sampling a signal generated from the data storage medium (Fig.10, sampler #212, Col 15, lines 19 – 46).

Re claim 16, McEwen teaches of the memory comprising a path memory of length M (L, Col 3, Lines 13 – 15).

Re claims 21 and 24, McEwen teaches of a data storage system (Col 1, Lines 20 – 34, Fig.10) comprising: an input line that provides a sampled channel sequence (Y_K, Col 10, Lines 35 - 45) (Fig.2); Veterbi detection means for determining a recovered sequence (Col 10, Line 55 - 57) from the sampled channel sequence (Y_K, Col 10, Lines 35 - 45), the Viterbi detection means including means for maximizing correlation (path metric corresponding to the maximum likelihood, V_{DATA}), minimizing a probability of making an error in determining the recovered sequence (Col 2, Lines 29 – 39). However, McEwen does not specifically show the correlation performed in the branch metric unit.

Akiyama teaches of a signal evaluation apparatus and signal evaluation method for evaluating a reproduction signal of a recording medium (Col 1, Lines 6 – 8).

Akiyama teaches of a branch metric $(Z_K - Y_K)^2$ of each branch metrics (correlation) where ZK is the obtained output sequences and YK is the estimated output sequences (Col 2, Lines 8 – 20) for a partial response channel (PRML, Col 1, Line 26 and Col 7, Lines 10 – 21); the estimated output sequence being estimated based on the partial response channel (Col 8, Lines 63 – 67);

It would have been obvious to one having ordinary skill in the art at the time the invention was made to have known that a branch metric unit would correlate the obtained output sequence with the estimated output sequence so that the survivor path memory would output the maximum likelihood sequence estimate VDATA.

Re claims 23 and 26, McEwen teaches of a head-disk assembly (#200, Fig.10, Col comprising the input line (Col 15, Lines 48 – 57).

Claim 14 is rejected under 35 U.S.C. 103(a) as being unpatentable over McEwen and Akiyama as applied to claim 13 above, and further in view of Cideciyan et al (US 6,377,635).

McEwen and Akiyama teach all the limitations of claim 13. However, they do not specifically show the equation for determining survivor paths.

Cideciyan teaches of methods and apparatus are provided for implementing high-speed and area efficient architectures for Viterbi detection of generalized partial response signals using both partial matched filter and matched filter metrics (Abstract, lines 1 – 4). Cideciyan also teaches of the polynomial of claim 15 (Col 4, lines 11 – 12)

as well as the correlation discussed in claim 13 (Col 4, Lines 15 – 20, equation 2) of the obtained output sequences (y_n) and estimated output sequences (sequence $\{\hat{a}_n\}$). The add-compare-select component (ACS unit, Col 3, Lines 44 – 55) compares paths and determines survivor paths by maximizing a quantity defined according to an equation (equation 3, first term), $\sum_{k=0}^{N} y_k \cdot y_k^*$, y_k corresponds to a real channel output, and y_k^* corresponds to an estimated channel output (Col 4, Lines 20 – 44).

It would have been obvious to one having ordinary skill in the art at the time the invention was made to have used the equation above to minimize the metric for simplifying the architecture/implementation of the Viterbi detector.

Claims 22 and 25 are rejected under 35 U.S.C. 103(a) as being unpatentable over McEwen and Akiyama as applied to claims 21 and 24 above, and further in view of Fisher et al (US 6,249,398).

McEwen and Akiyama teach all the limitations of claims 21 and 24. McEwen also teaches of gain amplification (change in the amplitude, #208, Col 15, Lines 36 – 40). Akiyama also teaches of an RF circuit for adjusting the amplitude (Col 7, Lines 22 – 25). However, McEwen and Akiyama do not specifically teach of providing robust tolerance of phase uncertainty with the widely varying amplitude waveform.

Fisher teaches of a new class of fixed partial response targets for use in a PRML magnetic medium read channel (Abstract, Lines 1 – 2). To properly equalize and detect

the user-data bits, several parameters must be adaptively controlled. The sample values at node 54 are applied to an error generator circuit (#64, Fig.2) where they are compared to the ideal target response values. These values are in accordance with a $7+4D-4D^2-5D^3-2D^4$ polynomial. These values are relative and they may be scaled to the A/D output bits as appropriate. The error generator (#64) provides an error signal via (#66) to gain control circuitry (varying amplitude, #68, Fig.2), which in turn controls the variable gain filter (#42). The error generator 64 also provides input via path 67 to timing control circuitry (#70), which in turn adjusts the sampling phase of the sampler (#46) (Fig.2, Col 6, Lines 20 – 43).

It would have been obvious to one having ordinary skill in the art at the time the invention was made to have provided a time control circuitry in order to control any phase changes for improving signal acquisition performance in a disk drive read channel.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Aristocratis Fotakis whose telephone number is (571) 270-1206. The examiner can normally be reached on Monday - Thursday 7 - 5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chieh Fan can be reached on (571) 272-3042. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

AF

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SUPERVISORY PATENT EXAMINER